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20987

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01/30/2006

VOLENTINE FRANCOS, & WHITT PLLC
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RESTON, VA 20190



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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/805,225	03/22/2004	Sun-Lee Hwang	SEC.1151	9135

TITLE OF INVENTION: METHOD OF ALIGNING A SEMICONDUCTOR SUBSTRATE WITH A SEMICONDUCTOR ALIGNMENT APPARATUS

APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1400	\$300	\$1700	05/01/2006

EXAMINER	ART UNIT	CLASS-SUBCLASS
CHAMBLISS, ALONZO	2814	438-005000

1. Change of correspondence address or indication of "Fee Address" (37 CFR 1.363).
- ☐ Change of correspondence address (or Change of Correspondence Address form PTO/SB/122) attached.
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2. For printing on the patent front page, list
- (1) the names of up to 3 registered patent attorneys or agents OR, alternatively,
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3. ASSIGNEE NAME AND RESIDENCE DATA TO BE PRINTED ON THE PATENT (print or type)

PLEASE NOTE: Unless an assignee is identified below, no assignee data will appear on the patent. If an assignee is identified below, the document has been filed for recordation as set forth in 37 CFR 3.11. Completion of this form is NOT a substitute for filing an assignment.

(A) NAME OF ASSIGNEE

Samsung Electronics Co., Ltd.

(B) RESIDENCE: (CITY and STATE OR COUNTRY)

Suwon-si, Gyeonggi-do, Republic of Korea

Please check the appropriate assignee category or categories (will not be printed on the patent): ☐ Individual ☒ Corporation or other private group entity ☐ Government

4a. The following fee(s) are enclosed:

- ☒ Issue Fee
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5. Change in Entity Status (from status indicated above)

- ☐ a. Applicant claims SMALL ENTITY status. See 37 CFR 1.27. ☐ b. Applicant is no longer claiming SMALL ENTITY status. See 37 CFR 1.27(g)(2).

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Authorized Signature

Typed or printed name

Adam C. Volentine

05/02/2006 SZEWDIE2 00000040 10005225
 Date May 1, 2006

01 FC:1501 1400.00 OP
 02 FC:1506 33289 300.00 OP
 03 FC:8001 3.00 OP

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TRANSMITTAL OF PAYMENT OF ISSUE FEE (Large Entity)

(37 CFR 1.431)

Docket No.

SEC.1151

Applicant(s): sUN-IEE hWANG

MAY 01 2006

Application No.

10/805,225

Filing Date

03/22/2004

Examiner

Chambliss, Alonzo

Customer No.

20987

Group Art Unit

2814

Confirmation No.

9135

Invention: METHOD OF ALIGNING A SEMICONDUCTOR SUBSTRATE WITH A SEMICONDUCTOR ALIGNMENT APPARATUS

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COMMISSIONER FOR PATENTS
P.O. Box 1450
Alexandria, VA 22313-1450

Transmitted herewith are the following for the above-identified application.

☒ Issue Fee Transmittal Form PTOL-85☒ Utility Fee: \$ 1400.00☐ Design Fee: _____☐ Plant Fee: _____☒ Publication Fee: \$ 300.00☒ A check in the amount of \$1,703.00 is attached.☒ The Director is hereby authorized to charge and credit Deposit Account No. 50-0238 as described below.☐ Charge the amount of☒ Credit any overpayment.☒ Charge any additional fee required.☐ Payment by credit card. Form PTO-2038 is attached.

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Signature

Dated: MAY 1, 2006

ADAM C. VOLENTINE
REG. NO. 33289

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